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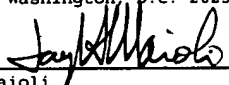
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Satoru KAWAKAMI  
Serial No.: 10/052,984  
Filed: November 7, 2001  
For: TUNER AND RECEIVER APPARATUS  
Group A.U.:

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I hereby certify that this paper is being deposited this date with the U.S. Postal Service in first class mail addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.	
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Jay H. Maioli Reg. No. 27,213	Date

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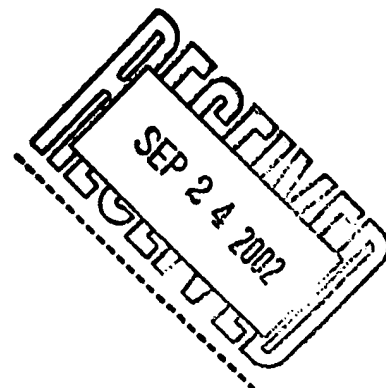
September 5, 2002  
1185 Avenue of the Americas  
New York, NY 10036  
(212) 278-0400

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

As a means of complying with the duty of disclosure set forth in 37 CFR § 1.56 and in keeping with the guidelines of 37 CFR § 1.98, Applicant hereby submits information thought to be relevant to the examination of the above-identified application. Also submitted herewith is a completed form PTO-1449.



This information came to light during the examination of a counterpart application in the European Patent Office in an Office Action dated August 2, 2002. Accordingly, the undersigned hereby certifies that the information submitted herewith is being submitted within three months from the date of that Office Action.

Applicant, through his undersigned attorney, hereby certifies that, unless submitted herewith, no English language translation is presently available to those individuals identified in 37 CFR § 1.56(c) for any non-English language reference(s) cited.

To the best of the undersigned's knowledge, no Office Action has yet been received in the above-identified application.

U.S. Patent No. 5,483,413 (Babb) apparently relates to an apparatus for controlling electromagnetic interference from multi-layered circuit boards.

European Patent No. 0 932 252 A2 (Sumi et al.) apparently relates to a tuning demodulator for digitally modulated RF signals.

"PCB design techniques for lowest-cost EMC compliance: Part 1" (M.K. Armstrong) apparently relates to PCB design techniques for lowest-cost EMC compliance.

German Patents DE 19649433 (Komatsu et al.) and DE 19630720 (Messmer) are in the German language. Submitted herewith is a copy of the European Search Report evidencing the relevance of these references.

No fee is deemed necessary in connection with the filing of this Information Disclosure Statement. However, if a fee is required for this submission, the Commissioner is authorized to charge the requisite fee to our Deposit Account No. 03-3125.

Respectfully submitted,  
COOPER & DUNHAM LLP

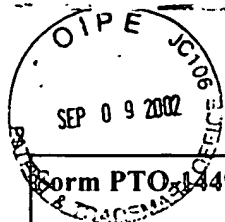


Jay H. Maioli  
Reg. No. 27,213

JHM/HYL

Enclosure

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Sheet 1 of 1

Form PTO-1449

U.S. Department of Commerce  
Patent and Trademark Office

Atty. Docket No.  
7217/65957

Serial No.  
10/052,984

LIST OF PRIOR ART CITED BY APPLICANT  
(Use several sheets if necessary)

Applicant  
Satoru Kawakami

Filing Date  
11/07/01

Group

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	AA	5 4 8 3 4 1 3	01/09/96	Babb	361	220	10/24/94
	AB						
	AC						
	AD						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AE	1 9 6 4 9 4 33	11/28/96	German Patent	H05K	9/00		X
	AF	1 9 6 3 0 7 20	7/30/96	German Patent	H05K	1/16		X
	AG	0 9 3 2 2 5 2	01/21/99	European Patent	H03D	7/16	X	
	AH							
	AI							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AL	Armstrong, M.K, PCB design techniques for lowest-cost EMC compliance: Part 1, <i>Electronics &amp; Communication Engineering Journal</i> , August 1999, pp.185-194						
	AM							
	AN							
	AO							
	AP							
	AQ							

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this from with next communication to applicant.